

FIG. 1 (BACKGROUND ART)

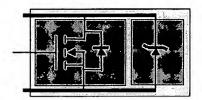


FIG. 2 (BACKGROUND ART)

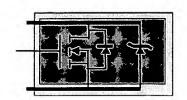


FIG. 3 (BACKGROUND ART)

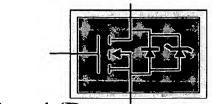
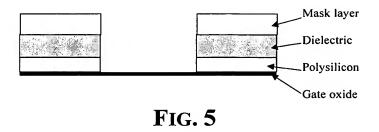


FIG. 4 (BACKGROUND ART)



Tilt angle

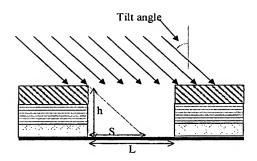
Mask layer

Dielectric

Polysilicon

Gate oxide

FIG. 6



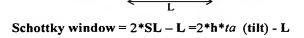


FIG. 7

FIG. 8

Schottky window

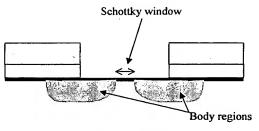


FIG. 9

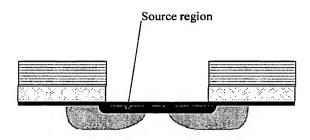


FIG. 10

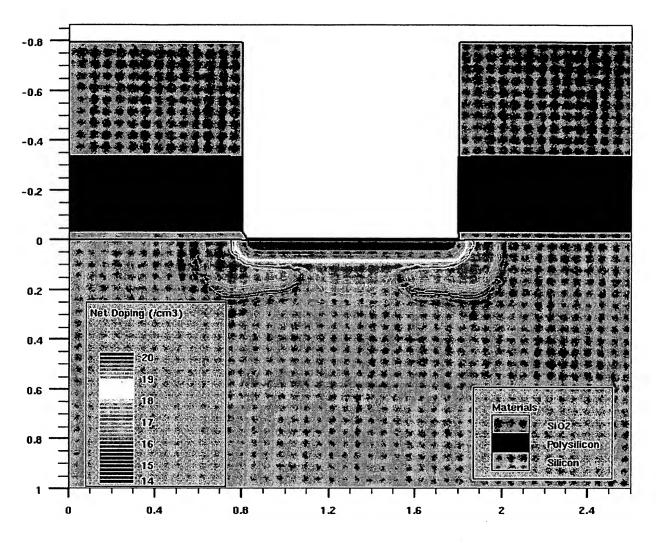


FIG. 11

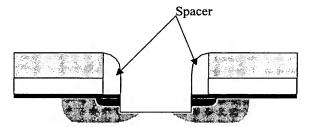


FIG. 12

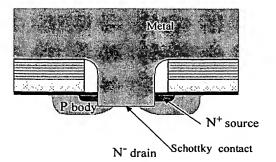


FIG. 13

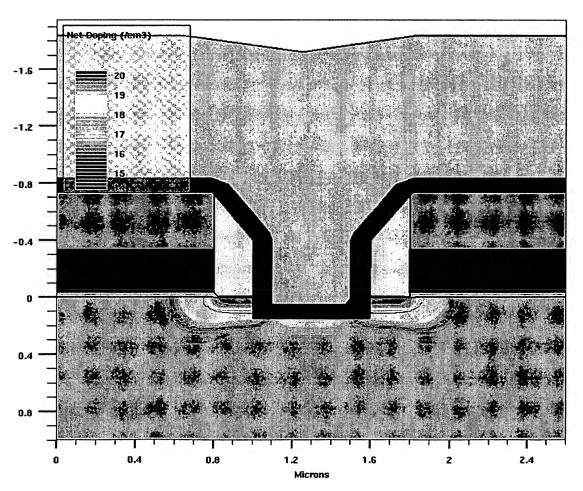


FIG. 14

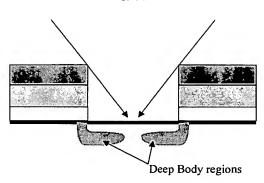


FIG. 15

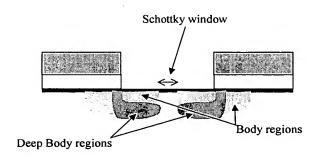


FIG. 16

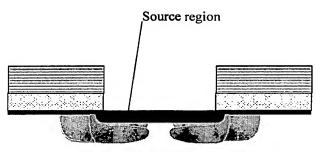
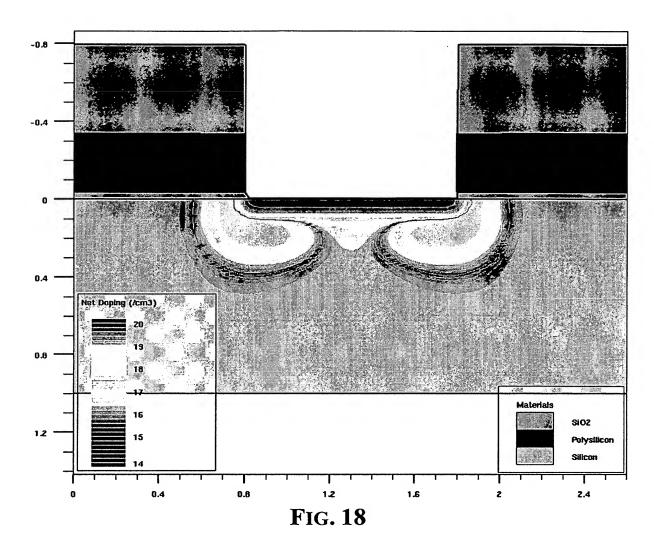


FIG. 17



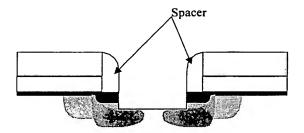


FIG. 19

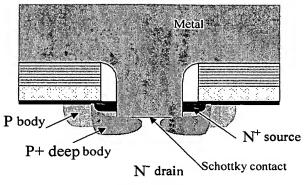
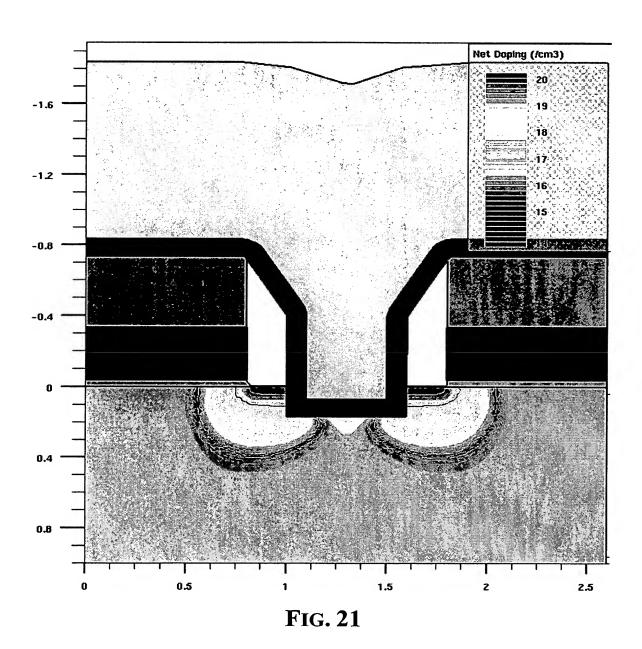


FIG. 20



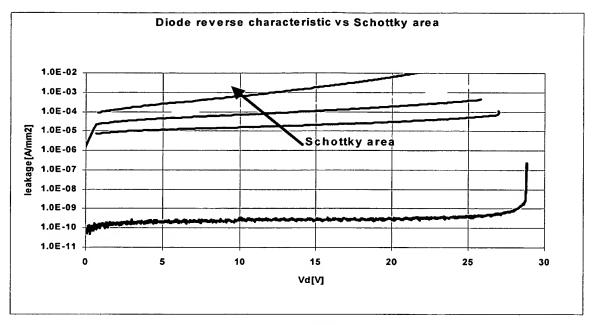


FIG. 22

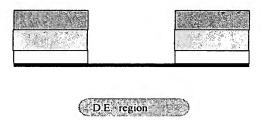


FIG. 23

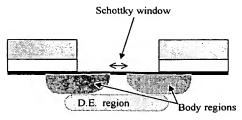


FIG. 24

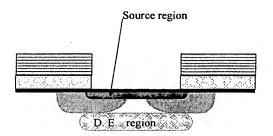
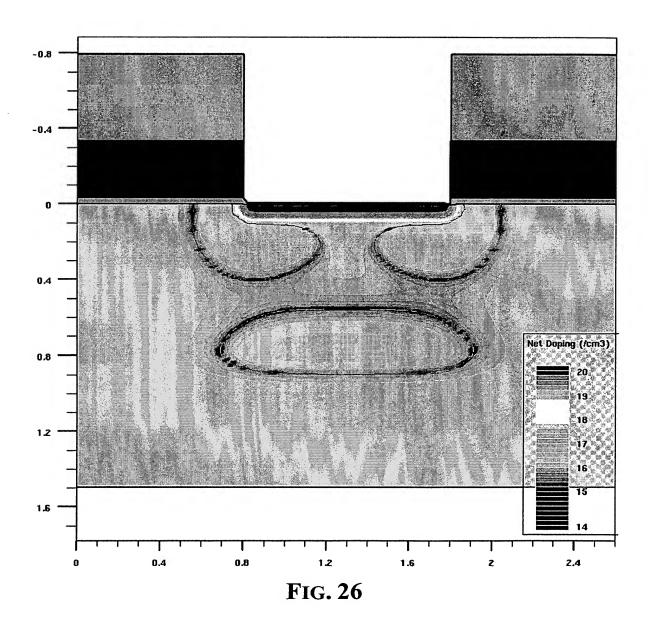
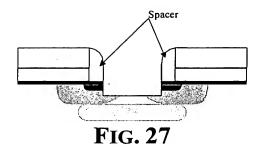


FIG. 25





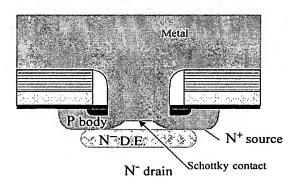
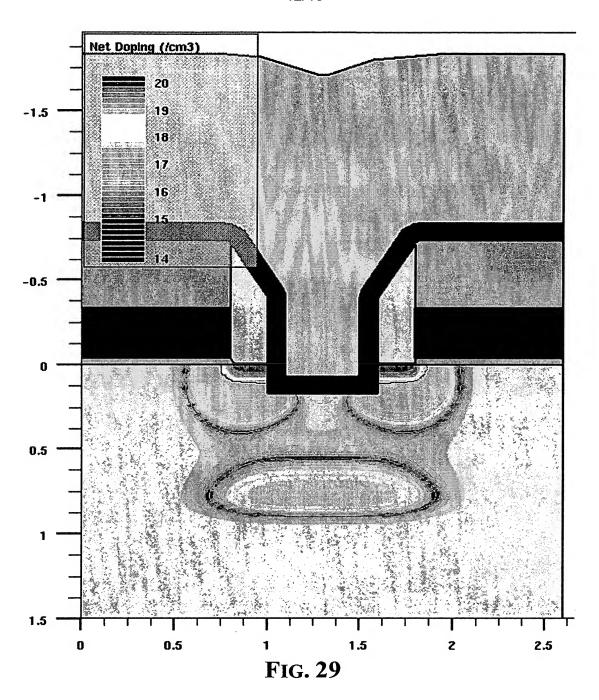


FIG. 28



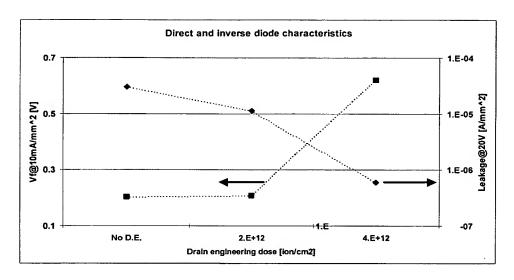


FIG. 30

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